



EXPEDITED PROCEDURE – EXAMINING GROUP 2188

S/N 10/659,920

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Raymond S. Tetrick	Examiner:	Reginald Bragdon
Serial No.:	10/659,920	Group Art Unit:	2188
Filed:	September 11, 2003	Docket No.:	884.A31US2
Title:	ADAPTIVE PREFETCH OF I/O DATA BLOCKS		
Assignee:	Intel Corporation	Customer No.:	21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

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This paper is in response to the Advisory Action mailed April 28, 2005. Additionally, this Amendment and Response is accompanied with a Request for Continued Examination (RCE). Please amend the application as follows: